Points-to Analysis for Java Based on Annotated Constraints

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Abstract

The goal of points-to analysis for Java is to determine the set of objects pointed to by a reference variable or a reference object field. In this paper we present a points-to analysis for Java based on Andersen’s points-to analysis for C [5].

Andersen’s analysis can be implemented efficiently by using systems of set-inclusion constraints and by employing several techniques for constraint representation and resolution. We extend these techniques to efficiently represent and solve systems of annotated inclusion constraints. The annotations play two roles in our analysis. Method annotations are used to model precisely and efficiently the semantics of virtual calls. Field annotations allow us to distinguish between different fields of an object. In addition, our analysis keeps track of all reachable methods and avoids analyzing irrelevant library code.

We evaluate the performance of the analysis on a large set of realistic Java programs. Our experiments show that the analysis runs in practical time and space, and has significant impact on call graph construction, virtual call resolution, synchronization removal, stack-based object allocation, and object read-write information. The results show that our analysis is a realistic candidate for a relatively precise, practical, general-purpose points-to analysis for Java.

1 Introduction

Although the Java programming language has gained widespread popularity, the performance of Java software is typically worse than the performance of software written in languages such as C and C++. Improving performance through the use of compiler technology is crucial for making Java a viable choice for production-strength software. In addition, the development of large Java software systems requires strong support from software engineering tools for program understanding, restructuring, and testing.

Optimizing compilers and software engineering tools employ various static analyses to determine properties of run-time program behavior. One fundamental static analysis is points-to analysis. For Java, points-to analysis determines the set of objects whose addresses may be stored in a given reference variable or a reference object field. By computing such points-to sets for variables and fields, the analysis constructs an abstraction of the run-time memory states of the analyzed program. This abstraction is typically represented by one or more points-to graphs. (An example of a points-to graph is shown in Figure 1, which is discussed later.)

Optimizing compilers for Java can use points-to analysis to perform a variety of popular optimizations such as virtual call resolution, removal of unnecessary synchronization, and stack-based object allocation. Typically, each of these optimizations is based on a specialized analysis designed for the purpose of this specific optimization. Thus, compilers that employ multiple optimizations need to implement many different analyses. In contrast, using a single general-purpose points-to analysis can enable several different optimizations. An important advantage of this approach is its practicality: the cost of the analysis is amortized across many client optimizations, and the costly development effort to implement the optimizations is significantly reduced.

Another advantage of using points-to analysis is that it enables a variety of other analyses—for example, side-effect analysis, which determines the memory locations that may be modified by the execution of a statement, and def-use analysis, which identifies pairs of statements that set the value of a memory location and subsequently use that value. Such analyses are needed by compilers to perform well-known optimizations such as code motion and partial redundancy elimination. These analyses are also important in the context of software engineering tools: for example, def-use analysis is needed for program slicing and data-flow-based testing. Such analyses and optimizations have been extensively investigated for other languages, but there has been little work on using them for Java. Points-to analysis is a crucial prerequisite for employing these analyses and optimizations, and will allow them to play an increasingly important role in optimizing compilers and software engineering tools for Java.

To make points-to analysis a useful component in realistic Java compilers and tools, the main challenge is to design analyses with the right balance between cost and precision. Fortunately, there is extensive previous experience in designing points-to analyses for C; this analysis expertise can be leveraged by adapting existing C analyses for Java. In this paper we define and evaluate a points-to analysis for Java based on Andersen’s points-to analysis for C [5]. Andersen’s analysis is a relatively precise flow- and context-insensitive analysis with cubic worst-case complexity. Previous work has shown that constraint-based implementations of this

\[1\] For example, for virtual call resolution, the compiler can use the points-to solution to identify virtual calls that have only one possible target method. Any such call can be replaced with a direct call, and subsequently the target method can be inlined.

\[2\] A flow-insensitive analysis ignores the flow of control between program points. A context-insensitive analysis does not distinguish between different invocations of a procedure.
analysis can be very efficient [17, 38]. These implementations are based on set-inclusion constraints of the form \( L \subseteq R \), where \( L \) and \( R \) are points-to sets. Their performance depends on several techniques for efficient constraint representation and constraint resolution.

The previous work on constraint-based implementations of Andersen’s analysis for C is insufficient for analysis of Java. First, the analysis must be adapted to model precisely the semantics of virtual calls by performing dispatch based on the class of the receiver object. In addition, since object fields in Java often point to other objects, the analysis must distinguish between the different fields of an object. This is analogous to separate tracking of the different fields of a C structure, which is not done by the existing constraint-based implementations of Andersen’s analysis for C. Our empirical results show that the previous constraint-based techniques do not perform well for Java unless augmented with more precise handling of object fields.

We extend and modify the existing constraint-based technology to efficiently represent and solve systems of annotated inclusion constraints. The annotated constraints are of the form \( L \subseteq_R a \), where \( a \) is an annotation. The annotations play two roles in our analysis. Method annotations are used to model precisely and efficiently the semantics of virtual calls, by representing the relationships between a virtual call, its receiver objects, and its target methods. Field annotations allow separate tracking of the flow of values through the different fields of an object. By using constraint annotations, we have been able to perform practical and precise points-to analysis for Java.

One disadvantage of Andersen’s analysis is the implicit assumption that all code in the program is executable. Java programs contain large portions of unused library code; including such dead code can have negative effects on analysis cost and precision. In our points-to analysis, we keep track of all methods reachable from the entry points of the program, and only analyze reachable methods.

We have implemented our analysis and evaluated its performance on a large set of realistic Java programs. On 16 out of the 23 data programs, analysis time is less than a minute. Even on large programs, the analysis runs in a few minutes and uses less than 200Mb of memory. To the best of our knowledge, these are the first published empirical results showing that a relatively precise points-to analysis can run in practical time and space for large Java programs. This practicality makes the analysis a realistic candidate for a relatively precise general-purpose points-to analysis for Java.

We evaluate the impact of the analysis on some of its possible client applications. Our experiments show significant improvement in the precision of the program call graph. Through profiling experiments, we also show that in many cases our analysis allows near-perfect resolution of virtual calls. Using the points-to solution, we can determine that on average 48% of the object allocation sites correspond to objects for which synchronization is unnecessary, and 29% of the sites correspond to objects that can be stack-allocated instead of heap-allocated. Our profiling experiments confirm that a large number of the objects created at run-time can be characterized as not needing synchronization or being allocatable on the stack. Finally, our measurements show very good analysis precision in determining the objects that can be read or written by program statements; this object read-write information is a crucial prerequisite for clients such as side-effect analysis and def-use analysis.

### Contributions

- We define a general-purpose points-to analysis for Java based on Andersen’s points-to analysis for C.
- We define annotated inclusion constraints and present techniques for efficient representation and resolution of such constraints.
- We show how to implement our points-to analysis using annotated inclusion constraints. The implementation models virtual calls and object fields precisely and efficiently, and only analyzes reachable methods.
- We evaluate the analysis on a large set of realistic Java programs. Our results show that the analysis runs in practical time and space, and has significant impact on call graph construction, virtual call resolution, synchronization removal, stack-based object allocation, and object read-write information.

### Outline

The rest of the paper is organized as follows. Section 2 defines the semantics of our points-to analysis. Section 3 discusses the applications of points-to analysis for Java. Section 4 describes the general structure of our annotated inclusion constraints, and Section 5 contains the details of our constraint-based points-to analysis. The experimental results are presented in Section 6. Section 7 discusses related work, and Section 8 presents conclusions and future work.

### 2 Semantics of Points-to Analysis for Java

In this section we define the semantics of our points-to analysis for Java by extending the semantics of Andersen’s points-to analysis for C. Section 5 describes the implementation of the analysis with annotated inclusion constraints.

The analysis is defined in terms of three sets. Set \( R \) contains all reference variables in the analyzed program. Set \( O \) contains names for all objects created at object allocation sites; for each allocation site \( s_i \), we use a separate object name \( o_i \). Set \( F \) contains all instance fields in program classes. Analysis semantics is expressed as manipulations of points-to graphs containing two kinds of edges. Edge \( (r, o_i) \in R \times O \) shows that reference variable \( r \) points to object \( o_i \). Edge \( (o_i, f) \in (O \times F) \times O \) shows that field \( f \) of object \( o_i \) points to object \( o_j \). A sample program and its points-to graph are shown in Figure 1.

![Figure 1: Sample program and its points-to graph.](image)

We assume that the program is represented by statements of the following form:

- Direct assignment: \( 1 = r \)
\[
\begin{align*}
(s_1 &: l = \text{new } C, G) &\Rightarrow G \cup \{(l, o_l)\} \\
(l &= r, G) &\Rightarrow G \cup \{(l, o_l) \mid o_l \in Pt(G, r)\} \\
(l, f &= r, G) &\Rightarrow \\
&\quad G \cup \{(l, o_l) \mid o_l \in Pt(G, l) \land o_l \in Pt(G, r)\} \\
(l &= r, f, G) &\Rightarrow \\
&\quad G \cup \{(l, o_l) \mid o_l \in Pt(G, r) \land o_l \in Pt(G, (o_j, f))\} \\
(l &= r, m(r_1, \ldots, r_n), G) &\Rightarrow \\
&\quad G \cup \{\text{resolve}(G, m, o_r, r_1, \ldots, r_n, l) \mid o_r \in Pt(G, r_0)\}
\end{align*}
\]

\textbf{3 Applications of Points-to Analysis for Java}

Points-to analysis has a wide range of client applications, which makes it a useful component of optimizing compilers and software engineering tools for Java. The cost of the analysis can be amortized across many client applications. Once implemented, the analysis can be reused by various clients at no additional development cost; such reusability is an important practical advantage. In this section we briefly discuss several specific applications of points-to analysis for Java. In our experiments, we have evaluated the impact of the analysis on some of these applications; the results from these experiments are presented in Section 6.

\textbf{Call Graph Construction and Virtual Call Resolution} The points-to solution can be used to determine the target methods of a virtual call by examining the classes of all possible receiver objects. The set of target methods is needed to construct the call graph for the analyzed program; this graph is a fundamental prerequisite for all interprocedural analyses and optimizations used in Java compilers and tools. If the call has only one target method, it can be resolved by replacing the virtual call with a direct call; this optimization eliminates the run-time overhead of virtual dispatch. In addition, virtual call resolution allows subsequent inlining of the target method, potentially enabling additional optimizations within the caller.

\textbf{Synchronization Removal} Synchronization in Java allows safe access to shared objects in multi-threaded programs. Each object has an associated lock which is used to ensure mutual exclusion. Synchronization operations on locks can have considerable run-time overhead; this overhead occurs even in single-threaded programs, because the standard Java libraries are written in a thread-safe manner.

Static analysis can be used to detect properties that allow the removal of unnecessary synchronization. For example, no synchronization is necessary when accessing an object that may never “escape” its creating thread (i.e., a thread-local object).

\textbf{Definition 1} Let \( o \) be an object and \( T \) be the thread that created \( o \). The object is thread-local if no thread \( T' \neq T \) can access \( o \).

Some escape analyses \([11, 7, 8, 43]\) have been used to identify thread-local objects and to remove the synchronization constructs associated with such objects. Points-to analysis can be used as an alternative to escape analysis in detecting thread-local objects. Consider an object \( o_i \) and suppose that in the points-to graph computed by the analysis, \( o_i \) is not reachable from (i) static (i.e., global) reference variables, or (ii) objects of classes that implement interface \texttt{java.lang.Runnable}\footnote{The \texttt{run} methods of such objects are the starting points of new threads.}. It can be proven that in this case \( o_i \) is not accessible outside the thread that created it; thus, we can identify such thread-local objects by traversing the points-to graph.

\textbf{Stack Allocation} In some cases, an object can be allocated on a method’s stack frame rather than on the heap. This transformation reduces garbage collection overhead and enables additional optimizations such as object reduction \([21]\).

Similarly to synchronization removal, static analysis can be used to detect properties that allow stack-based allocation. For example, stack allocation is possible for an object that may never “escape” the lifetime of its creating method (i.e., a method-local object).

\textbf{Definition 2} Let \( o \) be an object and \( M \) be the method that created \( o \). The object is method-local if \( o \) can only be accessed during the lifetime of \( M \).

Some escape analyses \([11, 7, 43]\) can detect method-local objects; clearly, such objects can be allocated on the stack frames of their creating methods. Points-to analysis can be used as an alternative to escape analysis in identifying method-local objects. Let object \( o_i \) be thread-local according to the conditions described above. Suppose that in the
points-to graph computed by the analysis, \( o_i \) is not reachable from the formal parameters or the return variable of the method that created \( o_i \). In this case, it can be proven that \( o_i \) is method-local; thus, we can identify such method-local objects by traversing the points-to graph.

**Object Read-Write Information** Points-to analysis can be used to determine which objects are read and/or written by every program statement. This information is required for a variety of other static analyses. For example, for the purposes of side-effect analysis, points-to information can be used to answer questions like “Can statement \( p.f = x \) modify the \( f \) field of any object pointed to by \( q \)?” As another example, points-to information is needed to answer questions like “Can statement \( z = q.f \) read any memory locations that are written by statement \( p.f = x \)?”, which are fundamental for def-use analysis.

Analyses that require read-write information are used in compilers to perform various advanced optimizations such as intraprocedural and interprocedural code motion and partial redundancy elimination. In addition, such analyses play a fundamental role in a variety of software engineering tools (e.g., in the context of program slicing or data-flow-based testing), thus enabling tool support for complex development tasks. Despite the extensive previous work on such analyses and optimizations for other languages, there has been little work on using them in Java compilers and tools. Practical and precise points-to analysis is crucial for enabling the use of these analyses and optimizations for Java.

4 Annotated Inclusion Constraints

This section describes the general structure of the annotated inclusion constraints used in our points-to analysis for Java. The details about the specific kinds of constraints and annotations are discussed in Section 5.

Previous work on Andersen’s analysis for C [17, 38] is based on non-annotated inclusion constraints and uses several techniques for efficient constraint representation and resolution. We extend this work by introducing annotations that allow us to model object fields and virtual calls in Java.

4.1 Constraint Language

We consider annotated set-inclusion constraints of the form \( L \subseteq_a R \), where \( a \) is chosen from a given set of annotations. We assume that one element of this set is designated as the empty annotation \( e \), and will use \( L \subseteq \) \( R \) to denote constraints labeled with \( i \). In our analysis, the annotations are used to model the flow of values through fields of objects, as well as the flow of values between a virtual call site and the run-time target methods of the call.

\( L \) and \( R \) are expressions representing sets, defined by the following grammar:

\[
L, R \rightarrow v \mid c(v_1, \ldots, v_n) \mid \text{proj}(c, i, v) \mid 0 \mid 1
\]

Here \( v \) and \( v_i \) are set variables, \( c(\ldots) \) are constructed terms and \( \text{proj}(\ldots) \) are projection terms. Each constructed term is built from an \( n \)-ary constructor \( c \). A constructor is either covariant or contravariant in each of its arguments; the role of this variance in constraint resolution will be explained shortly. Constructed terms may appear on both sides of inclusion relations. \( 0 \) and \( 1 \) represent the empty set and the universal set; they are treated as nullary constructors. Projections of the form \( \text{proj}(c, i, v) \) are terms used to select the \( i \)-th argument of any constructed term \( c(\ldots, v_i, \ldots) \).

\[
c(v_1, \ldots, v_n) \subseteq_a c(v_1', \ldots, v_n) \Rightarrow
\begin{cases}
  v_i \subseteq_a v'_i & \text{if } c \text{ is covariant in } i \\
  v'_i \subseteq_a v_i & \text{if } c \text{ is contravariant in } i
\end{cases}
\]

Figure 3: Resolution rules for structural constraints.

Projection terms may appear only on the right-hand side of an inclusion.

4.2 Annotated Constraint Graphs

Systems of constraints from the above language can be represented as directed multi-graphs. This representation is a natural extension of the graph representation for non-annotated constraints used to implement Andersen’s analysis for C [17]. Constraint \( L \subseteq_a R \) is represented by an edge from the node for \( L \) to the node for \( R \); the edge is labeled with the annotation \( a \). There could be multiple edges between the same pair of nodes, each with a different annotation.

The nodes in the graph can be classified as variables, sources, and sinks. Sources are constructed terms that occur on the left-hand side of inclusions. Sinks are constructed terms or projections that occur on the right-hand side of inclusions. The graph only contains edges that represent atomic constraints of the following forms: Source \( \subseteq_a \text{ Var} \), Var \( \subseteq_a \text{ Var} \), or Var \( \subseteq_a \text{ Sink} \). If the constraint system contains a structural (non-atomic) constraint, the resolution rules from Figure 3 are used to generate new atomic constraints, as described in Section 4.3.

We use annotated constraint graphs based on the inductive form representation [3]. Inductive form is an efficient sparse representation that does not explicitly represent the transitive closure of the constraint graph. The graphs are represented with adjacency lists \( \text{pred}(n) \) and \( \text{succ}(n) \) stored at each node \( n \). Edge \( (n_1, n_2, a) \), where \( a \) is an annotation, is represented either as a predecessor edge by having \( (n_1, a) \in \text{pred}(n_2) \), or as a successor edge by having \( (n_2, a) \in \text{succ}(n_1) \), but not both. Source \( \subseteq_a \text{ Var} \) is always a predecessor edge and Var \( \subseteq_a \text{ Sink} \) is always a successor edge. Var \( \subseteq_a \text{ Var} \) is either a predecessor or a successor edge, based on a fixed total order \( \tau \) : \( \text{Vars} \rightarrow N \). Edge \( (v_1, v_2, a) \) is a predecessor edge if and only if \( \tau(v_1) < \tau(v_2) \). The order function is typically based on the order in which variables are created as part of building the constraint system [38].

4.3 Solving Systems of Annotated Constraints

Every system of annotated inclusion constraints can be represented by an annotated constraint graph in inductive form. The system is solved by computing the closure of the graph under the following transitive closure rule:

\[
\begin{cases}
  (L, a) \in \text{pred}(v) \\
  (R, b) \in \text{succ}(v) \\
  \text{Match}(a, b)
\end{cases} \Rightarrow L \subseteq_c R \quad \text{(Trans)}
\]

The closure rule can be applied locally, by examining
pred(v) and succ(v). The new transitive constraint is created only if the annotations of the two existing constraints “match”—that is, only if Match(a, b) holds, where Match is a binary predicate on the set of annotations. Intuitively, the Trans rule uses the annotations to filter out some flow of values in the constraint system. The Match predicate is defined as follows:

\[
\text{Match}(a, b) = \begin{cases} 
\text{true} & \text{if } a \text{ or } b \text{ is the empty annotation } \epsilon \\
\text{true} & \text{if } a = b \\
\text{false} & \text{otherwise}
\end{cases}
\]

The annotation \(c\) of the new constraint is

\[
c = \begin{cases} 
a & \text{if } b = \epsilon \\
b & \text{if } a = \epsilon \\
\epsilon & \text{otherwise}
\end{cases}
\]

Intuitively, an annotation is propagated until it is matched with another instance of itself, after which the two instances cancel out.

If the new constraint generated by the Trans rule is atomic, a new edge is added to the graph. Otherwise, the resolution rules from Figure 3 are used to transform the constraint into several atomic constraints and their corresponding edges are added to the graph.

The closure of a constraint graph under the Trans rule is the solved inductive form of the corresponding constraint system. The least solution of the system is not explicit in the solved inductive form [3], but is easy to compute by examining all predecessors of each variable. For constraint graphs without annotations, the least solution \(LS(v)\) for a variable \(v\) is

\[
LS(v) = \{c[\ldots] \mid (c[\ldots]) \in \text{pred}(v)\} \cup \bigcup_{u \in \text{pred}(v)} LS(u)
\]

In this case, \(LS(v)\) can be computed by transitive acyclic traversal of all predecessor edges [17]. For an annotated constraint graph, the traversal is done similarly, but the annotations are used as in rule Trans:

\[
LS(v) = \{(c[\ldots], a) \mid (c[\ldots], a) \in \text{pred}(v)\} \cup
\{(c[\ldots], z) \mid \langle u, x \rangle \in \text{pred}(v) \land (c[\ldots], y) \in LS(u) \\
\land \text{Match}(x, y)\}
\]

Here annotation \(z\) is computed from annotations \(x\) and \(y\) as in the Trans rule.

5 Constraint-based Points-to Analysis for Java

In this section we show how to implement the points-to analysis from Section 2 using annotated inclusion constraints. Recall that the analysis is defined in terms of the set \(R\) of all reference variables and the set \(O\) of names for all objects created at object allocation sites. Every element of \((R \cup O)\) is essentially an abstract memory location representing a set of run-time memory locations.

To model the analysis with annotated inclusion constraints, we extend a technique developed for Andersen’s analysis for C [17, 38]. For each abstract location \(x\), a set variable \(v_x\) represents the set of abstract locations pointed to by \(x\). The representation of each location is through a trinary constructor ref which is used to build constructed terms of the form ref\((x, v_x, v_y)\). The last two arguments are the same variable, but with different variance—the overline notation is used to denote a contravariant argument. Intuitively, the second argument is used to read the values of locations pointed to by \(x\), while the last argument is used to update the values of locations pointed to by \(x\). Given a reference variable \(r \in R\) and an object variable \(o \in O\), constraint

\[
\text{ref}(o, v_o, \overline{v_o}) \subseteq v_r
\]

shows that \(r\) points to \(o\).

We use field annotations to model the flow of values through fields of objects. Field annotations are unique identifiers for all instance fields defined in program classes. For any two object variables \(o_1\) and \(o_2\), constraint

\[
\text{ref}(o_2, v_{o_2}, \overline{v_{o_2}}) \subseteq_f v_{o_1}
\]

shows that field \(f\) in object \(o_1\) points to object \(o_2\).

5.1 Constraints for Assignment Statements

For every program statement, our analysis generates annotated inclusion constraints representing the semantics of the statement. Figure 4 shows the constraints generated for assignment statements. The first two generation rules are straightforward. The rule for \(l = r\) uses the first constraint to access the points-to set of \(l\), and the second constraint to update the values of field \(f\) in all objects pointed to by \(l\). Similarly, the last rule uses two constraints to read the values of field \(f\) in all objects pointed to by \(r\).

Example Consider the statements in Figure 5 and their corresponding points-to graph. After processing the statements, our analysis creates the following constraints:

\[
\text{ref}(o_1, v_{o_1}, \overline{v_{o_1}}) \subseteq v_p \\
\text{ref}(o_2, v_{o_2}, \overline{v_{o_2}}) \subseteq v_q \\
v_p \subseteq \text{proj}(\text{ref}, 3, u) \quad v_q \subseteq_f u \\
v_p \subseteq \text{proj}(\text{ref}, 2, u) \quad w \subseteq_f v_r
\]

where \(u\) and \(w\) are fresh variables. For the purpose of this example we assume that the variable order \(\tau\) (defined in Section 4.2) is \(\tau(v_p) < \tau(v_q) < \tau(v_r) < \tau(v_{o_1}) < \tau(v_{o_2}) < \tau(u) < \tau(w)\). Consider the indirect write in \(p.f = q\). Since we have

\[
\text{ref}(o_1, v_{o_1}, \overline{v_{o_1}}) \subseteq v_p \subseteq \text{proj}(\text{ref}, 3, u)
\]
we can use the Trans rule and the resolution rules from Figure 3 to generate a new constraint $u \subseteq v_0$. Thus,

$$v_q \subseteq_f u \subseteq v_0$$
and using rule Trans we generate $v_q \subseteq_f v_0$. Intuitively, this new constraint shows that some of the values of field $f$ in object $o_1$ come from variable $q$. Now we have

$$ref(o_2, v_{o_2}, v_{o_2}) \subseteq v_q \subseteq_f v_0$$

Since both constraint edges are predecessor edges, we cannot apply rule Trans. Still, in the least solution of the constraint system (as defined in Section 4.3), we have the constraint $ref(o_2, v_{o_2}, v_{o_2}) \subseteq_f v_0$, which shows that field $f$ of $o_1$ points to $o_2$.

To model indirect reads, we use the second argument of the ref constructor. For example, for the constraints above we have

$$ref(o_1, v_{o_1}, v_{o_1}) \subseteq v_p \subseteq proj(ref, 2, w)$$

and therefore $v_{o_1} \subseteq w \subseteq_f v_r$, which through Trans generates $v_{o_1} \subseteq_f v_c$. This new constraint shows that the value of $r$ comes from field $f$ of object $o_1$. Now we have

$$v_q \subseteq_f v_{o_1} \subseteq_f v_r$$

Since the annotations of the two constraints match—that is, they represent access to the same field—we generate $v_q \subseteq v_r$ to represent the flow of values from $q$ to $r$. Thus, in the least solution of the system we have

$$ref(o_2, v_{o_2}, v_{o_2}) \subseteq v_r$$
which shows that reference variable $r$ points to $o_2$. This example illustrates how field annotations allow us to model flow of values through object fields.

5.2 Handling of Virtual Calls

For every virtual call in the program, our analysis generates a constraint according to the following rule:

$$\{(l = r_0.m(r_1, \ldots, r_k)) \Rightarrow \{v_{r_0} \subseteq_m lam(\theta, v_{r_1}, \ldots, v_{r_k}, v_l)\}\}$$

The rule is based on a lam (lambda) constructor. The constructor is used to build a term that encapsulates the actual arguments and the left-hand-side variable of the call. The annotation on the constraint is a unique identifier of the compile-time target method of the call. This annotation is used during the analysis to find all appropriate run-time target methods.

To model the semantics of virtual calls as defined in Section 2, we separately perform virtual dispatch for every receiver object pointed to by $r_0$. In order to do this efficiently, we use a precomputed lookup table. For a given receiver object at a virtual call site, the lookup table is used to determine the corresponding run-time target method, based on the class of the receiver object.$^4$ Such a table is straightforward to precompute by analyzing the class hierarchy; the table is essentially a representation of the dispatch function from Section 2.

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$^4$Every object is tagged with its class; this tag is used when performing lookups.

Given the class of the receiver object and the unique identifier for the compile-time target of the virtual call, the lookup table returns a lambda term of the form

$$lam(v_{p_0}, v_{p_1}, \ldots, v_{p_k}, v_{ret})$$

Here $p_i$ are the formal parameters of the run-time target method; $p_0$ corresponds to the implicit parameter this. We assume that each method has a unique variable ref that is assigned the value returned by the method. At the beginning of the analysis, lambda terms of this form are created for all non-abstract methods in the program and are stored in the lookup table.

To model the effects of virtual calls, we define an additional closure rule Virtual. This rule encodes the semantics of virtual calls described in Section 2 and is used together with the Trans rule to obtain the solved form of the constraint system. Virtual is applied whenever we have two constraints of the form

$$ref(o, v_{o}, v_{o}) \subseteq v \quad v \subseteq_m lam(\theta, v_{r_1}, \ldots, v_{r_k}, v_l)$$

As described in Section 4.2, the edge from the ref term is a predecessor edge, and the edge to the lam term is a successor edge. Thus, the Virtual closure rule can be applied locally, by examining sets pred$(v)$ and succ$(v)$. Whenever two such constraints are detected, the lookup table is used to find the lambda term for the run-time method corresponding to object $o$ and compile-time target method $m$. The result of applying Virtual are two new constraints:

$$ref(o, v_{o}, v_{o}) \subseteq v_{p_0} \quad lam(v_{p_0}, v_{p_1}, \ldots, v_{p_k}, v_{ret}) \subseteq v_{p_0}$$

The first constraint creates the association between parameter this of the invoked method and the receiver object. The second constraint immediately resolves to $v_{r_i} \subseteq v_{p_i}$ (for $i \geq 1$) and $v_{ret} \subseteq v_i$, plus the trivial constraint $\theta \subseteq v_{p_0}$. These new atomic constraints model the flow of values from actuals to formals, as well as the flow of return values to the left-hand-side variable $l$ used at the call site.

Example Consider the set of statements in Figure 6. For the purpose of this example, assume that $\tau(v_{x_1}) < \tau(v_{c_1}) < \tau(v_{y})$. Since the declared type of $b$ is $\mathbb{A}$, at call site $c_1$ the compile-time target method is $\mathbb{A}.n$; thus, we have

$$v_{x_1} \subseteq_{\mathbb{A}.n} lam(\theta, v_{x_2})$$

When rule Virtual is applied as shown in (1), the lookup for receiver object $o_2$ and compile-time target $\mathbb{A}.n$ produces run-time target $\mathbb{B}.n$. The resolution with the lam term for $\mathbb{B}.n$ creates the two new constraints shown in (1).

The declared type of $c$ is $\mathbb{B}$, and for call site $c_2$ we have

$$ref(o_2, v_{o_2}, v_{o_2}) \subseteq v_{c_2} \quad v_{c} \subseteq_{\mathbb{B}.n} lam(\theta, v_{y})$$

where the first constraint is obtained through the Trans rule.$^5$ By applying Virtual, we create the constraints shown in (2). For call site $c_3$, the receiver object can be either $o_1$ or $o_2$. As shown in (3) and (4), separate lookup and resolution is performed for each receiver.

$^5$We use $\mathbb{X}.z$ to denote method $z$ defined in class $\mathbb{X}$.

Note that if $\tau(v_{x_1}) < \tau(v_{c_1})$, instead of propagating the ref term to $v_{c_1}$ we would propagate the lam term to $v_{y}$.
_index
class A { X n() { ... return rA; } }
class B extends A
    { X n() { ... return rB; } }
s1: A a = new A();
s2: A b = new B();
    B c = b;
c1: X x = b.n();
c2: X y = c.n();
    if (...) a = b;
c3: X z = a.n();
(1) ref(a2, v2, v2) \subseteq v2 \subseteq A.n lamB(v2)
    \Rightarrow
    \{ ref(a2, v2, v2) \subseteq vB.n.this, vB \subseteq v2 \}
(2) ref(a2, v2, v2) \subseteq v2 \subseteq A.n lamB(v2)
    \Rightarrow
    \{ ref(a2, v2, v2) \subseteq vB.n.this, vB \subseteq v2 \}
(3) ref(a1, v1, v2) \subseteq v2 \subseteq B.n lamB(v2)
    \Rightarrow
    \{ ref(a1, v1, v2) \subseteq vA.n.this, vA \subseteq v2 \}
(4) ref(a2, v2, v2) \subseteq v2 \subseteq A.n lamB(v2)
    \Rightarrow
    \{ ref(a2, v2, v2) \subseteq vB.n.this, vB \subseteq v2 \}

Figure 6: Example of virtual call resolution.

5.3 Correctness
For every program statement, our analysis generates constraints representing the semantics of the statement. This initial constraint system is solved by closing the corresponding constraint graph under closure rules TRANS and VIRTUAL. Let $A^*$ be the solved inductive form of the constraint system. Recall that the least solution of the system is not explicit in $A^*$ and can be obtained through additional traversal of predecessor edges, as described in Section 4.3.

Let $G^*$ be the points-to graph computed by the algorithm in Section 2. Consider a reference variable $r$ and an object variable $o$ such that $(r, o) \in G^*$. It can be proven that the least solution constructed from $A^*$ contains the constraint $ref(o, v_o, v_o) \subseteq v_r$. Similarly, consider two object variables $o_i$ and $o_j$ such that $((o_i, f), (o_j, f)) \in G^*$; it can be proven that the least solution contains $ref(o_i, v_{o_i}, v_{o_i}) \subseteq f v_{o_i}$.

The proof of the above claims depends on the following restriction on the variable order $\tau$: all variables $v_r$, where $r \in R$, should have lower order than the rest of the constraint variables. We enforce this restriction as part of building the constraint system. Given this restriction, it can be proven that the least solution of the constraint system represents all points-to pairs from $G^*$. The proof of this claim is outlined in Appendix A.

5.4 Cycle Elimination and Projection Merging
Cycle elimination [17] and projection merging [38] are two important techniques for reducing the cost of Andersen’s analysis for C. In this section we show how these techniques can be adapted for the annotated constraints used in our analysis for Java.

The idea behind cycle elimination is to detect a set of variables that form a cycle in the constraint graph:

$$v_1 \subseteq v_2 \subseteq \ldots \subseteq v_k \subseteq v_1$$

Clearly, all such variables have equal solutions and can be replaced with a single variable. Whenever a cycle is detected during the resolution process, one variable from the cycle is chosen as a witness variable, and the rest of the variables are redirected to the witness. This transformation has no effect on the computed solution, but can significantly reduce the cost of the analysis.

Cycle detection is performed every time a new edge is added between two variables $v_i$ and $v_j$. The detection algorithm essentially performs depth-first traversal of the constraint graph and tries to determine whether $v_i$ is reachable from $v_j$. Cycle detection is partial and does not detect all cycles. Nevertheless, for Andersen’s analysis for C this technique has significant impact on the running time of the analysis (see [17] for more details).

Cycle elimination cannot be reused in a straightforward manner for the annotated constraint systems presented in this paper. If we performed the standard cycle detection, we would discover cycles in which some edges have field annotations; however, the variables in such cycles do not have the same solution, and cannot be replaced by a single witness variable. To guarantee the correctness of our analysis for Java, we use a restricted form of cycle elimination.

The cycle detection algorithm is the same as in [17], but is invoked only when a new edge is added between two reference variables—that is, when the new edge is $(v_i, v_j)$, where $v_i$, $v_j \in R$. It can be proven that in this case, the detected cycle contains only reference variables, and all edges in the cycle have empty annotations. This guarantees that all variables on the cycle have identical points-to sets, and therefore replacing the cycle with a single variable preserves the points-to solution.

Projection merging is a technique for reducing redundant edge additions in the constraint system [38]. It combines multiple projection constraints for the same variable into a single projection constraint. For example, constraints $v \subseteq proj(c, i, u_1)$ and $v \subseteq proj(c, i, u_2)$ are replaced by $w \subseteq proj(c, i, w)$, where $w$ is a special projection variable. For points-to analysis for C, constraints of the form $w \subseteq u_1$ are represented only as successor edges; this restriction guarantees a bound on the number of projection variables $w$. The analysis ensures the restriction by assigning to $w$ a high index in the variable order $\tau$. Moreover, projection merging is beneficial only when coupled with cycle elimination (see [38] for details).

In our annotated constraint systems, projection merging does not interact with cycle elimination. In our case, the high indices from [38] (which necessitate the interaction) are not required. The high indices become unnecessary because the bound on the number of special projection variables is ensured by the variable ordering restriction from Section 5.3. Thus, the special projection variables can be treated similarly to the rest of the variables in the constraint system. This decoupled form of projection merging has significant impact on the running time of the analysis for Java.

5.5 Tracking Reachable Methods
Andersen’s analysis implicitly assumes that all code in the program is executable. Since Java programs heavily use libraries that contain many unused methods, we have augmented our analysis to keep track of all reachable methods, in order to avoid analyzing dead code. Thus, we take into account the effects of statements in a method body only if the
method has been shown to be reachable from one of the entry methods of the program. The set of entry methods $M_0$ contains (i) the main method of the starting class, (ii) the methods invoked at JVM startup (e.g., `initializeSystemClass`), and (iii) the class initialization methods `<clinit>` containing the initializers for static fields [28, Section 3.9].

We augment the semantic rules from Section 2 to include this reachability computation. The original rules are of the form $(s, G) \Rightarrow G'$, and define the effects of statement $s$ on points-to graph $G$. The augmented rules have the form $(s, G, M) \Rightarrow (G', M')$, where $M$ and $M'$ are sets of reachable methods. If the method that contains $s$ is not in $M$, we have $G' = G$ and $M' = M$. Otherwise, $G'$ is defined as in Figure 2, and $M'$ contains all elements of $M$ plus all methods that become reachable when $s$ is a call statement. Any call to a constructor also generates a corresponding call to the appropriate `finalize` method. For multi-threaded programs, a call to `Thread.start` is treated as a call to the corresponding run method.

We implement this augmented semantics by maintaining a list of reachable methods, initialized with $M_0$. Whenever a method becomes reachable, all statements in its body are processed and the appropriate constraints are introduced in the constraint system. It is easy to show that this approach produces a safe solution with respect to the augmented semantics.

5.6 Analysis Implementation

We use the Soot framework (www.sable.mcgill.ca), version 1.0.0, to process Java bytecode and to build a typed intermediate representation [42]. The constraint-based analysis uses BANE (Berkeley ANalysis Engine) [2]. BANE is a toolkit for constructing constraint-based program analyses. The public distribution of BANE (bane.cs.berkeley.edu) contains an efficient constraint-solving engine that employs induc-tive form [3], cycle elimination [17], and projection merging [38]. We modified the constraint engine to attach annotations to the constraints, to implement the appropriate resolution and closure rules, and to perform cycle elimination and projection merging as described in Section 5.4. The analysis works on top of the constraint engine, by processing newly discovered reachable methods and generating the appropriate constraints. The points-to effects of JVM startup code and native methods (for JDK 1.1.8) are encoded in stubs included in the analysis input. Dynamic class loading (e.g., through `Class.forName`) and reflection (e.g., calls to `Class.newInstance`) are resolved manually; similar approaches are typical for static whole-program compilers and tools [25, 19, 40, 41].

6 Empirical Results

All experiments were performed on a 360MHz Sun Ultra-60 machine with 512Mb physical memory. The reported times are the median values out of three runs. We used 23 publicly available data programs, ranging in size from 56Kb to about 1Mb of bytecode. We used programs from the SPEC JVM98 suite, other benchmarks used in previous work on analysis for Java, as well as programs from an Internet archive (www.jars.com) of popular publicly available Java applications.

Table 1 shows some characteristics of the data programs. The first two columns show the number of user (i.e., non-library) classes and their bytecode size. The next three columns show the size of the program, including library classes, after using class hierarchy analysis (CHA) [13] to filter out irrelevant classes and methods. The number of methods is essentially the number of nodes in the call graph computed by CHA. The last column shows the number of statements in Soot’s intermediate representation.

### Table 1: Characteristics of the data programs.

<table>
<thead>
<tr>
<th>Program</th>
<th>User Class</th>
<th>Size (Kb)</th>
<th>Whole-program</th>
</tr>
</thead>
<tbody>
<tr>
<td>proxy</td>
<td>18</td>
<td>56.6</td>
<td>566</td>
</tr>
<tr>
<td>compress</td>
<td>22</td>
<td>76.7</td>
<td>508</td>
</tr>
<tr>
<td>db</td>
<td>14</td>
<td>70.7</td>
<td>565</td>
</tr>
<tr>
<td>lib-6.1</td>
<td>21</td>
<td>55.6</td>
<td>574</td>
</tr>
<tr>
<td>echo</td>
<td>17</td>
<td>66.7</td>
<td>577</td>
</tr>
<tr>
<td>raytrace</td>
<td>35</td>
<td>115.9</td>
<td>582</td>
</tr>
<tr>
<td>ntt</td>
<td>35</td>
<td>115.9</td>
<td>582</td>
</tr>
<tr>
<td>jbar-2.1</td>
<td>64</td>
<td>185.2</td>
<td>618</td>
</tr>
<tr>
<td>jlex-1.2</td>
<td>25</td>
<td>95.1</td>
<td>578</td>
</tr>
<tr>
<td>javacup-0.10</td>
<td>33</td>
<td>127.3</td>
<td>581</td>
</tr>
<tr>
<td>rabbit-2</td>
<td>52</td>
<td>157.4</td>
<td>615</td>
</tr>
<tr>
<td>jack</td>
<td>67</td>
<td>191.5</td>
<td>613</td>
</tr>
<tr>
<td>jflex-1.2.2</td>
<td>54</td>
<td>198.2</td>
<td>608</td>
</tr>
<tr>
<td>jess</td>
<td>160</td>
<td>454.2</td>
<td>715</td>
</tr>
<tr>
<td>npegaudio</td>
<td>62</td>
<td>176.8</td>
<td>608</td>
</tr>
<tr>
<td>jtree-1.0</td>
<td>72</td>
<td>212.9</td>
<td>620</td>
</tr>
<tr>
<td>sablcc-2.9</td>
<td>312</td>
<td>532.4</td>
<td>684</td>
</tr>
<tr>
<td>javac</td>
<td>182</td>
<td>614.7</td>
<td>769</td>
</tr>
<tr>
<td>creature</td>
<td>86</td>
<td>229.0</td>
<td>626</td>
</tr>
<tr>
<td>mindterm1.1.5</td>
<td>129</td>
<td>461.1</td>
<td>686</td>
</tr>
<tr>
<td>soot-1.beta.4</td>
<td>677</td>
<td>1070.4</td>
<td>1274</td>
</tr>
<tr>
<td>maclin-0.9.2</td>
<td>235</td>
<td>655.2</td>
<td>824</td>
</tr>
<tr>
<td>javacc-1.0</td>
<td>63</td>
<td>502.6</td>
<td>615</td>
</tr>
</tbody>
</table>

Analysis Cost Our first set of experiments measured the cost of the analysis, as shown in Table 2. The first two columns show the running time of the analysis and the amount of memory used. For 16 out of the 23 programs, the analysis runs in less than a minute. For all programs, the running time is less than six minutes and the memory usage is less than 180Mb. To the best of our knowledge, these are the first published empirical results showing that a relatively precise points-to analysis can run in a few minutes on large realistic Java programs, using reasonable amounts of memory. Empirical results for more expensive points-to analyses [23, 9] suggest that they do not scale; for example, the 1-1-CFA analysis from [23] runs in about 2000 seconds on javacup and runs out of memory on javac.

The empirical results indicate that our analysis is practical in terms of running time and memory usage, as evidenced on a large set of Java programs. This practicality shows that the analysis is a realistic candidate for a relatively precise general-purpose points-to analysis for Java.

Performance can be improved further by techniques such as variable substitution [31], which have proven effective in reducing the cost of Andersen’s analysis for C; we intend to investigate this approach in our future work. In addition, analysis cost can be reduced if the library code is analyzed in advance. This would allow analysis information about the Java libraries to be computed once and subsequently used multiple times for different client programs; this is another promising direction for future work.

We also investigated a version of our analysis in which...
no field annotations are used, and therefore individual object fields are not distinguished. In essence, this no-fields version of the analysis is a straightforward extension of the best existing constraint-based implementations of Andersen’s analysis for C (recall that these implementations do not distinguish structure fields) [17, 38]. In this version we used the original cycle elimination and projection merging instead of the modified versions from Section 5.4. These experiments investigated the performance achievable through simple reuse of the already existing constraint-based techniques, without introducing our field annotations.

The last two columns in Table 2 show the cost of the no-fields version of the analysis. The running time is between 88% and 12056% (average 892%, median 384%) of the running time of the annotations-based analysis; the memory usage is between 112% and 437% (215% on average). Typically, the no-fields version is significantly more expensive than our analysis; for one of the larger programs, it even ran out of memory.

We draw two conclusions from these experiments. First, simple reuse of the constraint-based techniques for C is not sufficient: good performance for C does not guarantee good performance for Java. Second, it is crucial to distinguish object fields: the improved precision produces smaller points-to sets, which significantly reduces analysis cost. By using field annotations, we have been able to distinguish object fields in a simple and practical manner.

**Call Graph Construction and Virtual Call Resolution**

To measure the precision of the analysis with respect to call graph construction and virtual call resolution, we compared our points-to analysis with Rapid Type Analysis (RTA) [6]. RTA is an inexpensive and widely used analysis for call graph construction. It performs a reachability computation on the call graph generated by CHA; by keeping track of the classes that have been instantiated, RTA computes a more precise call graph than CHA.

Both our analysis and RTA improve the call graph computed by CHA by identifying sets of methods reachable from the entry points of the program; this reachability computation reduces the number of nodes in the call graph. For brevity, we summarize this reduction without explicitly showing the number of nodes for each program. The average reduction in the number of nodes is 54% for our analysis and 47% for RTA. On average, the call graph computed by our analysis has 14% less nodes than the call graph computed by RTA. This reduction allows subsequent analyses and optimizations to safely ignore large portions of the program.

To determine the improvement for call graph edges, we considered call sites that could not be resolved to a single target method by CHA. Let \( V \) be the set of all CHA-unresolved call sites that occur in methods identified by our analysis as reachable. For our data programs, the size of \( V \) is between 7% and 44% (22% on average) of all virtual call sites in reachable methods. For each site from \( V \), we computed the difference between the number of target methods according to CHA and the number of target methods according to RTA and our analysis. The average differences are shown in the first section of Table 3. On average, our analysis removes more than twice as many targets as RTA; this improved precision is beneficial for reducing the cost and improving the precision of subsequent interprocedural analyses.

The second section of Table 3 shows the percentage of call sites from \( V \) that were resolved to a single target method. Our points-to analysis performs significantly better than RTA—on average, 53% versus 15% of the virtual call sites are resolved. The increased precision allows better removal of run-time virtual dispatch and additional method inlining.

We performed additional experiments to estimate the potential performance impact of analysis precision on virtual
call resolution. These experiments used a subset of our data programs for which we had representative input data. For each program, we instrumented the user classes (i.e., non-library classes) and measured the number of times each call site was executed during a profile run of the program. Column (a) in Table 4 shows the total number of invocations of CHA-unresolved call sites. This number is an indicator of the run-time overhead of virtual dispatch, as well as the missed opportunities for performance improvement through inlining. We also measured what percentage of this total number was contributed by call sites that are uniquely resolved by RTA and by our analysis. These percentages are shown in column (b) and (c) in Table 4. Higher percentages indicate higher potential for performance improvement if the analyses are used during the compilation and optimization of the user classes.

The results from this profiling experiment indicate that RTA has little potential for improving the run-time performance over CHA. Our analysis shows significantly higher potential, and for several programs it allows the resolution of the majority of run-time virtual calls. In addition, we used the profile to determine what CHA-unresolved call sites had only one run-time target. Column (c) shows the contribution of such sites to the total count from column (a). This number is an upper bound on the number of invocations that could be resolved by a static analysis. By comparing columns (b) and (c), it is clear that in many cases our analysis achieves performance close to the best possible performance.

Synchronization Removal and Stack Allocation In addition to call graph construction and virtual call resolution, points-to analysis produces information that is useful for a variety of other applications: for example, synchronization removal, stack allocation, side-effect analysis, and def-use analysis. To investigate the impact of our analysis on synchronization removal and stack allocation, we identified all object allocation sites that correspond to thread-local and method-local objects, as described in Section 3. Figure 7(a) shows what percentage of all allocation sites in reachable programs for which we had representative input data. For each program, we instrumented the user classes (i.e., non-library classes) and measured the number of times each call site was executed during a profile run of the program. Column (a) in Table 4 shows the total number of invocations of CHA-unresolved call sites. This number is an indicator of the run-time overhead of virtual dispatch, as well as the missed opportunities for performance improvement through inlining. We also measured what percentage of this total number was contributed by call sites that are uniquely resolved by RTA and by our analysis. These percentages are shown in column (b) and (c) in Table 4. Higher percentages indicate higher potential for performance improvement if the analyses are used during the compilation and optimization of the user classes.

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Object Read-Write Information  We performed measurements to estimate the potential impact of our analysis on clients of object read-write information (e.g., side-effect analysis and def-use analysis). In particular, we considered all expressions of the form $p.f$ occurring in statements in reachable methods. For each such indirect access expression, the points-to set of $p$ contains all objects that may be read or written by the corresponding statement. More precise points-to analyses produce smaller numbers of accessed objects, which in turn results in better precision for the clients of the read-write information. Thus, to estimate the potential impact of our analysis, we measured the number of accessed objects for each indirect access expression; similar metrics have been traditionally used for points-to analysis for C.

Table 5 shows the distribution of the number of accessed objects; each column corresponds to a specific range of numbers. For example, the first column corresponds to expressions that may only access a single object, while the last column corresponds to expressions that may access at least 10 objects. Each column shows what percentage of all indirect access expressions corresponds to the particular range of numbers of accessed objects.

The measurements in Table 5 indicate that our analysis produces precise read-write information. Typically, more than half of the indirect accesses are resolved to a single object (which is the lower bound for this metric), and the majority of accesses are resolved to at most three objects. These results show that our analysis is a promising candidate for producing read-write information for clients such as side-effect analysis and def-use analysis.

7 Related Work

Points-to analysis for object references in Java is clearly related to pointer analysis for imperative languages such as C. There are various pointer analyses for C with different tradeoffs between cost and precision [26, 24, 16, 5, 33, 44, 36, 35, 27, 20, 12, 18, 10]. The closest related work from this category are the efficient constraint-based implementations of Andersen’s analysis [17, 38], which use the inductive form representation together with efficient resolution techniques such as cycle elimination and projection merging. Our work extends this approach by introducing constraint annotations and by modifying the graph representation and the resolution procedure. Field annotations allow us to track object fields separately; this is not possible with the constraints from [17, 38]. Method annotations allow us to model the semantics of virtual calls. In addition, we avoid analyzing dead library code by including a reaching computability computation in the analysis.

Constraint indices and constraint polarities [18] have been used to introduce context-sensitivity in unification-based flow analysis. This work has similar flavor to our use of annotations for tracking flow of values through object fields. Conceptually, in both cases the annotations model restricted value flow in constraint systems—either for unification-based constraints in [18], or for inclusion constraints in our case.

Recent work [37], which postdates our initial report [32], describes points-to analysis for Java based on Andersen’s analysis for C. Analysis running times are several times slower than ours, and memory usage is significantly higher. We believe that our low memory consumption is due to Chatterjee et al. [9]. This flow- and context-sensitive analysis is more precise than ours, but it is unclear whether it will scale for programs as large as our data programs. Points-to analyses with different degrees of precision have been proposed in the context of a framework for call graph construction in object-oriented languages [23]. The closest to our work is the 1-1-CFA algorithm, which incorporates flow- and context-sensitive points-to analysis. The data set from [23] contains few Java programs, and the analysis times suggest that 1-1-CFA is not practical. Our empirical results on a large set of realistic Java applications show that our analysis is a practical alternative to the more expensive analyses from [23, 9].

Class analysis for object-oriented languages computes a set of classes for each program variable; this set approximates the classes of all run-time values for this variable. The traditional client applications of class analysis are call graph construction and virtual call resolution. In general, points-to analysis subsumes class analysis; practical implementations of points-to analysis can be used for a variety of applications, including call graph construction and virtual call resolution. DeFouw et al. [14] present a family of practical interprocedural class analyses. Other work in this area considers more precise and costly analyses with some degree of context- or flow-sensitivity [29, 1, 30, 15, 23], as well as less precise but inexpensive analyses such as RTA [6, 41, 39]. There is a large body of work on synchronization removal and stack-based object allocation [4, 11, 7, 8, 43, 21, 34]. Gay and Steensgaard [21] present a unification-based analysis for stack allocation. Ruf [34] describes an unification-based algorithm for synchronization removal. Aldrich et al. [4] develop several algorithms for synchronization removal based on 1-1-CFA; the scalability and usability of these analyses can be significantly improved if our points-to analysis is used instead of 1-1-CFA. Previous work on escape analysis for Java [11, 7, 8, 43] also investigates synchroniza-
tion removal and stack allocation; the scalability of these approaches remains unclear, since only [7] reports analysis times. In contrast to these specialized analyses, we have a practical general-purpose points-to analysis that can be used by a variety of client applications, including synchronization removal and stack allocation.

8 Conclusions and Future Work

Points-to analysis has a wide range of client applications, which makes it a useful component of optimizing compilers and software engineering tools for Java. We present a points-to analysis for Java based on Andersen’s points-to analysis for C. Our work extends existing constraint-based techniques by introducing constraint annotations. Method annotations are used to model precisely and efficiently the semantics of virtual calls. Field annotations allow us to distinguish between different fields of an object; this analysis feature is crucial for reducing analysis cost. On a large set of realistic Java programs, our experiments show that the analysis cost is practical. Through static and profiling measurements, we show that the points-to solution has significant impact on call graph construction, virtual call resolution, synchronization removal, stack-based object allocation, and object read-write information. Our results show that the analysis is a realistic candidate for a relatively precise, practical, general-purpose points-to analysis for Java.

One direction of future work is to investigate techniques for further reducing the cost of the analysis. For example, various kinds of approximations could reduce the cost without significant (or any) loss of precision (e.g., through techniques such as variable substitution [31]). In addition, analysis cost can be reduced if the library code is analyzed in advance. This would allow analysis information about the Java libraries to be computed once and subsequently used multiple times for different client programs.

Another direction of work is to investigate the impact of the analysis solution on traditional client analyses such as def-use analysis and side-effect analysis, which in turn are necessary for various well-known optimizations (e.g., code motion and partial redundancy elimination). Such analyses and optimizations have been extensively investigated in other languages, and will play an increasingly important role in aggressive optimizing compilers for Java.

Finally, it would be interesting to investigate applications of points-to analysis in the context of software engineering tools: for example, def-use analysis for program slicing and data-flow-based testing. The functionality provided by such tools will be an important advantage in the development of production-strength Java software systems.

9 Acknowledgments

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References

A Proof of Correctness

Let $G$ be a points-to graph as defined in Section 2 and $A$ be an annotated constraint graph in inductive form as described in Section 5. We will define a representation relation $\alpha$ between $A$ and the edges in $G$. If $\alpha$ holds between $A$ and all edges in $G$, we will write $\alpha(A,G)$.

Consider a reference variable $r$ and an object variable $o$ such that $e = (r,o)$ is an edge in $G$. We have $\alpha(A,e)$ if and only if $A$ contains a path

$$\text{ref}(o, v_o, \overrightarrow{o}) \rightarrow v_1 \rightarrow \ldots \rightarrow v_n \rightarrow v_r$$

such that all $v_i$ correspond to reference variables and all edges are predecessor edges with empty annotations.

Similarly, consider two object variables $o_1$ and $o_2$ such that $e = (\langle o_1, f \rangle, o_2)$ is an edge in $G$. We have $\alpha(A,e)$ if and only if at least one of the following two conditions is true. The first condition is that $A$ contains a path

$$\text{ref}(o_1, v_{o_1}, \overrightarrow{o_1}) \rightarrow v_1 \rightarrow \ldots \rightarrow v_n \xrightarrow{f} v_{o_2}$$

such that all $v_i (1 \leq i \leq n)$ correspond to reference variables, all edges are predecessor edges, and the only non-empty annotation on the path is $f$ on edge $(v_n, v_{o_2})$. The second condition is that $A$ contains a path

$$\text{ref}(o_j, v_{o_j}, \overrightarrow{o_j}) \rightarrow v_1 \rightarrow \ldots \rightarrow v_n \xrightarrow{f} u \rightarrow v_{o_i}$$

such that all $v_i (1 \leq i \leq n)$ correspond to reference variables and $u$ is a “fresh” variable created due to field access (see Figure 4). In this path, all edges are predecessor edges, and the only non-empty annotation is $f$ on edge $(v_n, u)$.

Let $G^*$ be the final points-to graph computed by the algorithm in Section 2, and $A^*$ be the solved inductive form of the corresponding annotated constraint system. The least solution of the constraint system is obtained through additional traversal of predecessor edges in $A^*$, as described in Section 4.3.

Suppose that $\alpha(A^*, G^*)$. It is easy to show that in this case $G^*$ is contained in the set of points-to pairs extracted from the least solution of the constraint system. For example, for every edge $(\langle o_1, f \rangle, o_2) \in G^*$, the least solution contains $\text{ref}(o_1, v_{o_1}, \overrightarrow{o_1}) \xrightarrow{f} v_{o_2}$. To prove that $\alpha(A^*, G^*)$, we use the following theorem:

**Theorem 1** Let $\langle s, G \rangle \Rightarrow G^*$ as described in Figure 2. If $\alpha(A,G)$, there exists a sequence of applications of closure rules and resolution rules such that $A$ can be transformed into $A'$ for which $\alpha(A', G^*)$.

The proof of this claim requires case-by-case analysis of all statement kinds.

**Case 1.** Consider the assignment $l = r$. In $G^*$, there are new edges of the form $(l, o_i)$, where $(r, o_i) \in G$. In $A'$, we have a path from $\text{ref}(o_1, v_{o_1}, \overrightarrow{o_1})$ to $v_r$ containing only
predecessor edges with empty annotations. Graph \( A \) also contains \( v_r \subseteq v_i \), which is represented either as a predecessor edge or as a successor edge (depending on the order of \( v_r \) and \( v_i \)). If \( v_r \in \text{pred}(v_i) \), then we have the needed path from \( \text{ref}(\alpha_i, v_{o_i}, v_{f_{o_i}}) \) to \( v_i \). If \( v_i \in \text{succ}(v_r) \), then a sequence of applications of rule \( \text{Trans} \) creates the needed path.

**Case 2.** Consider the assignment \( l = r \cdot f \). In \( G' \), there are new edges of the form \((l, o_j)\), where \((r, \alpha_i) \in G\) and \((\langle \alpha_i, f \rangle, o_j) \in G\). In \( A \) we have a path \( \text{ref}(\alpha_i, v_{o_i}, v_{f_{o_i}}) \rightarrow \ldots \rightarrow v_r \) containing only predecessor edges with empty annotations. Therefore \( A \) contains a path

\[
\text{ref}(\alpha_i, v_{o_i}, v_{f_{o_i}}) \rightarrow v_1 \rightarrow \ldots \rightarrow v_n \xrightarrow{f} v_{o_i}
\]

such that all \( v_i (1 \leq i \leq n) \) correspond to reference variables, all edges are predecessor edges, and the only non-empty annotation on the path is \( f \) on edge \((v_n, v_{o_i})\). \( A \) also contains constraints \( v_r \subseteq \text{proj}(\text{ref}, 2, u) \) and \( u \subseteq f v_1 \) (see Figure 4), which are represented as successor edges. Therefore we have

\[
\text{ref}(\alpha_i, v_{o_i}, v_{f_{o_i}}) \rightarrow \ldots \rightarrow v_r \rightarrow \text{proj}(\text{ref}, 2, u)
\]

and after a sequence of applications of \( \text{Trans} \) followed by the resolution rules for structural constraints shown on Figure 3, we have \( v_{o_i} \subseteq u \).

In the case when \( \tau(v_{o_i}) < \tau(u) \), we have \( v_{o_i} \in \text{pred}(u) \) and \( \langle u, f \rangle \in \text{succ}(u) \), which results in an \( f \)-annotated successor edge that represents constraint \( v_{o_i} \subseteq_f u \). Combining this edge with the \( f \)-annotated predecessor edge on the path from \( \text{ref}(\alpha_i, v_{o_i}, v_{f_{o_i}}) \) to \( v_{o_i} \) results in the needed path. Similarly, if \( \tau(v_{o_i}) > \tau(u) \), we have \( \langle v_n, f \rangle \in \text{pred}(v_{o_i}) \) and \( u \in \text{succ}(v_{o_i}) \), which results in an \( f \)-annotated predecessor edge that represents constraint \( v_n \subseteq_f u \). Combining this edge with the \( f \)-annotated successor edge representing \( u \subseteq_f v_i \) creates the needed path.

Note that the variable ordering restriction described in Section 5.3 ensures that the appropriate kinds of edges (predecessor or successor) are created when transforming \( A \). For example, suppose that \( \tau(u) < \tau(v_i) \) for Case 2 from above. Transforming \( A \) according to the applicable rules would create a path from \( \text{ref}(\alpha_i, v_{o_i}, v_{f_{o_i}}) \) to \( v_i \) which contains edges with non-empty annotations; in contrast, the needed path from \( \text{ref}(\alpha_i, v_{o_i}, v_{f_{o_i}}) \) consists of predecessor edges with empty annotations. Therefore \( \alpha(A', G') \) cannot be guaranteed without the variable ordering restriction from Section 5.3.

The rest of the cases in the proof are handled in a similar manner. Given the above theorem, it is trivial to show that \( \alpha \) holds between \( A' \) and \( G' \).